Overview

The KEMET ArcShield high voltage surface mount capacitors in X7R dielectric are designed for use in high voltage applications susceptible to surface arcing (arc-over discharge).

The phenomenon of surface arcing is caused by a high voltage gradient between the two termination surfaces or between one of the termination surfaces and the counter internal electrode structure within the ceramic body. It occurs most frequently at application voltages that meet or exceed 300 V, in high humidity environments, and in chip sizes with minimal bandwidth separation (creepage distance). This phenomenon can either damage surrounding components or lead to a breakdown of the dielectric material, ultimately resulting in a short circuit condition (catastrophic failure mode).

Patented ArcShield technology features KEMET's highly reliable base metal dielectric system, combined with a unique internal shield electrode structure that is designed to suppress an arc-over event while increasing available capacitance. Developed on the principle of a partial Faraday cage, this internal system offers unrivaled performance and reliability when compared to external surface coating technologies.

For added reliability, KEMET's flexible termination technology is an available option that provides superior flex performance over standard termination systems. This technology was developed to address flex cracks, which are the primary failure mode of MLCCs and typically the result of excessive tensile and shear stresses produced during board flexure and thermal cycling. Flexible termination technology inhibits the transfer of board stress to the rigid body of the MLCC, therefore mitigating flex cracks which can result in low IR or short circuit failures.

The KEMET ArcShield high voltage surface mount MLCCs are available in automotive grade, which undergo stricter testing protocol and inspection criteria. Whether underhood or in-cabin, these devices are designed for mission and safety-critical automotive circuits or applications requiring proven, reliable performance in harsh environments. Automotive grade devices meet the demanding Automotive Electronics Council's AEC-Q200 qualification requirements.

Ordering Information

<table>
<thead>
<tr>
<th>C</th>
<th>0603</th>
<th>W</th>
<th>392</th>
<th>K</th>
<th>C</th>
<th>R</th>
<th>A</th>
<th>C</th>
<th>TU</th>
</tr>
</thead>
<tbody>
<tr>
<td>Ceramic</td>
<td>Case Size (L” x W”)</td>
<td>Specification/ Series</td>
<td>Capacitance Code (pF)</td>
<td>Capacitance Tolerance</td>
<td>Rated Voltage (VDC)</td>
<td>Dielectric</td>
<td>Failure Rate/ Design</td>
<td>Termination Finish</td>
<td>Packaging/ Grade (C-Spec)</td>
</tr>
<tr>
<td>0603</td>
<td>0805</td>
<td>1206</td>
<td>1210</td>
<td>1808</td>
<td>1812</td>
<td>1825</td>
<td>2220</td>
<td>2225</td>
<td></td>
</tr>
<tr>
<td>V = ArcShield</td>
<td>W = ArcShield with flexible termination</td>
<td>Two significant digits and number of zeros.</td>
<td>J = ±5%</td>
<td>K = ±10%</td>
<td>M = ±20%</td>
<td>C = 500</td>
<td>B = 630</td>
<td>D = 1,000</td>
<td>R = X7R</td>
</tr>
</tbody>
</table>

1 Additional termination finish options may be available. Contact KEMET for details.
2 SnPb termination finish option is not available on automotive grade product.
2 Additional reeling or packaging options may be available. Contact KEMET for details.
Packaging C-Spec Ordering Options Table

<table>
<thead>
<tr>
<th>Packaging Type</th>
<th>Commercial Grade 1</th>
<th>Automotive Grade 2</th>
</tr>
</thead>
<tbody>
<tr>
<td>Bulk Bag</td>
<td>Not Required (Blank)</td>
<td>AUTO</td>
</tr>
<tr>
<td>7” Reel/Unmarked</td>
<td>7411 (EIA 0603 and smaller case sizes)</td>
<td>AUTO7411 (EIA 0603 and smaller case sizes)</td>
</tr>
<tr>
<td>7” Reel/Marked</td>
<td>7210 (EIA 0805 and larger case sizes)</td>
<td>AUTO7210 (EIA 0805 and larger case sizes)</td>
</tr>
<tr>
<td>13” Reel/Unmarked</td>
<td>7040 (EIA 0603)</td>
<td>7081</td>
</tr>
<tr>
<td>7” Reel/Unmarked/2 mm pitch 2</td>
<td>7215 (EIA 0805 and larger case sizes)</td>
<td>3190</td>
</tr>
<tr>
<td>13” Reel/Unmarked/2 mm pitch 2</td>
<td>7082</td>
<td>3191</td>
</tr>
</tbody>
</table>

1 Default packaging is "Bulk Bag." An ordering code C-Spec is not required for "Bulk Bag" packaging.

1 The terms "Marked" and "Unmarked" pertain to laser marking option of capacitors. All packaging options labeled as "Unmarked" will contain capacitors that have not been laser marked.

2 The 2 mm pitch option allows for double the packaging quantity of capacitors on a given reel size. This option is limited to EIA 0603 (1608 metric) case size devices. For more information regarding 2 mm pitch option see "Tape & Reel Packaging Information."

3 Reeling tape options (paper or plastic) are dependent on capacitor case size (L x W) and thickness dimension. See "Chip Thickness/Tape & Reel Packaging Quantities" and "Tape & Reel Packaging Information."

3 For additional Information regarding "AUTO" C-Spec options, see "Automotive C-Spec Information."

3 All Automotive packaging C-Specs listed exclude the option to laser mark components. Please contact KEMET if you require a laser marked option. For more information see "Capacitor Marking."

Benefits

- Patented technology
- Permanent internal arc protection
- Protective surface coating not required
- Base metal electrode (BME) dielectric system
- Industry leading CV values
- -55°C to +125°C operating temperature range
- Exceptional performance at high frequencies
- Lead (Pb)-free, RoHS and REACH compliant
- EIA 0603, 0805, 1206, 1210, 1808, 1812, 1825, 2220, and 2225 case sizes
- DC voltage ratings of 500 V, 630 V and 1 KV
- Capacitance offerings ranging from 1,000 pF to 560 nF
- Available capacitance tolerances of ±5%, ±10% and ±20%
- Low ESR and ESL
- Non-polar device, minimizing installation concerns
- Commercial and Automotive (AEC-Q200) grades available
- 100% pure matte tin-plated termination finish allowing for excellent solderability
- SnPb plated termination finish option available upon request (5% Pb minimum)
- Flexible termination option available upon request
Automotive C-Spec Information

KEMET automotive grade products meet or exceed the requirements outlined by the Automotive Electronics Council. Details regarding test methods and conditions are referenced in document AEC–Q200, Stress Test Qualification for Passive Components. These products are supported by a Product Change Notification (PCN) and Production Part Approval Process warrant (PPAP).

Automotive products offered through our distribution channel have been assigned an inclusive ordering code C-Spec, “AUTO.” This C-Spec was developed in order to better serve small and medium-sized companies that prefer an automotive grade component without the requirement to submit a customer Source Controlled Drawing (SCD) or specification for review by a KEMET engineering specialist. This C-Spec is therefore not intended for use by KEMET OEM automotive customers and are not granted the same “privileges” as other automotive C-Specs. Customer PCN approval and PPAP request levels are limited (see details below.)

Product Change Notification (PCN)
The KEMET product change notification system is used to communicate primarily the following types of changes:
• Product/process changes that affect product form, fit, function, and/or reliability
• Changes in manufacturing site
• Product obsolescence

<table>
<thead>
<tr>
<th>KEMET Automotive C-Spec</th>
<th>Customer Notification Due To:</th>
<th>Days Prior To Implementation</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Process/Product change</td>
<td>Obsolescence*</td>
</tr>
<tr>
<td>KEMET assigned¹</td>
<td>Yes (with approval and sign off)</td>
<td>Yes</td>
</tr>
<tr>
<td>AUTO</td>
<td>Yes (without approval)</td>
<td>Yes</td>
</tr>
</tbody>
</table>

¹ KEMET assigned C-Specs require the submittal of a customer SCD or customer specification for review. For additional information contact KEMET.

Production Part Approval Process (PPAP)
The purpose of the Production Part Approval Process is:
• To ensure that supplier can meet the manufacturability and quality requirements for the purchased parts.
• To provide the evidence that all customer engineering design records and specification requirements are properly understood and fulfilled by the manufacturing organization.
• To demonstrate that the established manufacturing process has the potential to produce the part.

<table>
<thead>
<tr>
<th>KEMET Automotive C-Spec</th>
<th>PPAP (Product Part Approval Process) Level</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>1</td>
</tr>
<tr>
<td>KEMET assigned¹</td>
<td>●</td>
</tr>
<tr>
<td>AUTO</td>
<td>●</td>
</tr>
</tbody>
</table>

¹ KEMET assigned C-Specs require the submittal of a customer SCD or customer specification for review. For additional information contact KEMET.

● Part number specific PPAP available
○ Product family PPAP only
Applications

Typical applications include switch mode power supplies (input filters, resonators, tank circuits, snubber circuits, output filters), high voltage coupling and DC blocking, lighting ballasts, voltage multiplier circuits, DC/DC converters and coupling capacitors in Ćuk converters. Markets include power supply, LCD fluorescent backlight ballasts, HID lighting, telecom equipment, industrial and medical equipment/control, LAN/WAN interface, analog and digital modems, and automotive (electric and hybrid vehicles, charging stations and lighting) applications.

Application Notes

X7R dielectric is not recommended for AC line filtering or pulse applications.

Qualification/Certification

Commercial Grade products are subject to internal qualification. Details regarding test methods and conditions are referenced in Table 4, Performance & Reliability.

Automotive Grade products meet or exceed the requirements outlined by the Automotive Electronics Council. Details regarding test methods and conditions are referenced in document AEC–Q200, Stress Test Qualification for Passive Components. For additional information regarding the Automotive Electronics Council and AEC–Q200, please visit their website at www.aecouncil.com.

Environmental Compliance

Lead (Pb)-free, RoHS, and REACH compliant without exemptions (excluding SnPb termination finish option).
### Dimensions – Millimeters (Inches) – Standard Termination

![SMD MLCCs diagram]

<table>
<thead>
<tr>
<th>EIA Size Code</th>
<th>Metric Size Code</th>
<th>L Length</th>
<th>W Width</th>
<th>T Thickness</th>
<th>B Bandwidth</th>
<th>S Separation Minimum</th>
<th>Mounting Technique</th>
</tr>
</thead>
<tbody>
<tr>
<td>0603</td>
<td>1608</td>
<td>1.60 (0.063) ±0.15 (0.006)</td>
<td>0.80 (0.032) ±0.15 (0.006)</td>
<td>0.35 (0.014) ±0.15 (0.006)</td>
<td>0.70 (0.028)</td>
<td>Solder Wave or Solder Reflow</td>
<td></td>
</tr>
<tr>
<td>0805</td>
<td>2012</td>
<td>2.00 (0.079) ±0.20 (0.008)</td>
<td>1.25 (0.049) ±0.20 (0.008)</td>
<td>0.50 (0.02) ±0.25 (0.010)</td>
<td>0.75 (0.030)</td>
<td>Solder Reflow Only</td>
<td></td>
</tr>
<tr>
<td>1206</td>
<td>3216</td>
<td>3.20 (0.126) ±0.20 (0.008)</td>
<td>1.60 (0.063) ±0.20 (0.008)</td>
<td>0.50 (0.02) ±0.25 (0.010)</td>
<td>0.75 (0.030)</td>
<td>Solder Reflow Only</td>
<td></td>
</tr>
<tr>
<td>1210</td>
<td>3225</td>
<td>3.20 (0.126) ±0.20 (0.008)</td>
<td>2.50 (0.098) ±0.20 (0.008)</td>
<td>0.50 (0.02) ±0.25 (0.010)</td>
<td>0.75 (0.030)</td>
<td>Solder Reflow Only</td>
<td></td>
</tr>
<tr>
<td>1808</td>
<td>4520</td>
<td>4.70 (0.185) ±0.50 (0.020)</td>
<td>2.00 (0.079) ±0.20 (0.008)</td>
<td>See Table 2 for Thickness</td>
<td>0.60 (0.024) ±0.35 (0.014)</td>
<td>N/A</td>
<td></td>
</tr>
<tr>
<td>1812</td>
<td>4532</td>
<td>4.50 (0.177) ±0.30 (0.012)</td>
<td>3.00 (0.118) ±0.30 (0.012)</td>
<td>See Table 2 for Thickness</td>
<td>0.60 (0.024) ±0.35 (0.014)</td>
<td>N/A</td>
<td></td>
</tr>
<tr>
<td>1825</td>
<td>4564</td>
<td>4.50 (0.177) ±0.30 (0.012)</td>
<td>6.40 (0.252) ±0.40 (0.016)</td>
<td>See Table 2 for Thickness</td>
<td>0.60 (0.024) ±0.35 (0.014)</td>
<td>N/A</td>
<td></td>
</tr>
<tr>
<td>2220</td>
<td>5650</td>
<td>5.70 (0.224) ±0.40 (0.016)</td>
<td>5.00 (0.197) ±0.40 (0.016)</td>
<td>See Table 2 for Thickness</td>
<td>0.60 (0.024) ±0.35 (0.014)</td>
<td>N/A</td>
<td></td>
</tr>
<tr>
<td>2225</td>
<td>5664</td>
<td>5.60 (0.220) ±0.40 (0.016)</td>
<td>6.40 (0.248) ±0.40 (0.016)</td>
<td>See Table 2 for Thickness</td>
<td>0.60 (0.024) ±0.35 (0.014)</td>
<td>N/A</td>
<td></td>
</tr>
</tbody>
</table>

### Dimensions – Millimeters (Inches) – Flexible Termination

<table>
<thead>
<tr>
<th>EIA Size Code</th>
<th>Metric Size Code</th>
<th>L Length</th>
<th>W Width</th>
<th>T Thickness</th>
<th>B Bandwidth</th>
<th>S Separation Minimum</th>
<th>Mounting Technique</th>
</tr>
</thead>
<tbody>
<tr>
<td>0603</td>
<td>1608</td>
<td>1.60 (0.064) ±0.17 (0.007)</td>
<td>0.80 (0.032) ±0.15 (0.006)</td>
<td>0.45 (0.018) ±0.15 (0.006)</td>
<td>0.58 (0.023)</td>
<td>Solder Wave or Solder Reflow</td>
<td></td>
</tr>
<tr>
<td>0805</td>
<td>2012</td>
<td>2.00 (0.079) ±0.30 (0.012)</td>
<td>1.25 (0.049) ±0.30 (0.012)</td>
<td>0.50 (0.02) ±0.25 (0.010)</td>
<td>0.75 (0.030)</td>
<td>Solder Reflow Only</td>
<td></td>
</tr>
<tr>
<td>1206</td>
<td>3216</td>
<td>3.30 (0.130) ±0.40 (0.016)</td>
<td>1.60 (0.063) ±0.35 (0.013)</td>
<td>0.60 (0.024) ±0.25 (0.010)</td>
<td>0.75 (0.030)</td>
<td>Solder Reflow Only</td>
<td></td>
</tr>
<tr>
<td>1210</td>
<td>3225</td>
<td>3.30 (0.130) ±0.40 (0.016)</td>
<td>2.60 (0.102) ±0.30 (0.012)</td>
<td>0.60 (0.024) ±0.25 (0.010)</td>
<td>0.75 (0.030)</td>
<td>Solder Reflow Only</td>
<td></td>
</tr>
<tr>
<td>1808</td>
<td>4520</td>
<td>4.70 (0.185) ±0.50 (0.020)</td>
<td>2.00 (0.079) ±0.20 (0.008)</td>
<td>See Table 2 for Thickness</td>
<td>0.70 (0.028) ±0.35 (0.014)</td>
<td>N/A</td>
<td></td>
</tr>
<tr>
<td>1812</td>
<td>4532</td>
<td>4.50 (0.178) ±0.40 (0.016)</td>
<td>3.20 (0.126) ±0.30 (0.012)</td>
<td>See Table 2 for Thickness</td>
<td>0.70 (0.028) ±0.35 (0.014)</td>
<td>N/A</td>
<td></td>
</tr>
<tr>
<td>1825</td>
<td>4564</td>
<td>4.50 (0.181) ±0.40 (0.016)</td>
<td>6.40 (0.252) ±0.40 (0.016)</td>
<td>See Table 2 for Thickness</td>
<td>0.70 (0.028) ±0.35 (0.014)</td>
<td>N/A</td>
<td></td>
</tr>
<tr>
<td>2220</td>
<td>5650</td>
<td>5.90 (0.232) ±0.75 (0.030)</td>
<td>5.00 (0.197) ±0.40 (0.016)</td>
<td>See Table 2 for Thickness</td>
<td>0.70 (0.028) ±0.35 (0.014)</td>
<td>N/A</td>
<td></td>
</tr>
<tr>
<td>2225</td>
<td>5664</td>
<td>5.90 (0.232) ±0.75 (0.030)</td>
<td>6.40 (0.248) ±0.40 (0.016)</td>
<td>See Table 2 for Thickness</td>
<td>0.70 (0.028) ±0.35 (0.014)</td>
<td>N/A</td>
<td></td>
</tr>
</tbody>
</table>
Surface Mount Multilayer Ceramic Chip Capacitors (SMD MLCCs)
ArcShield Technology, High Voltage, X7R Dielectric, 500 – 1,000 VDC (Commercial & Automotive Grade)

Electrical Parameters/Characteristics

<table>
<thead>
<tr>
<th>Item</th>
<th>Parameters/Characteristics</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operating Temperature Range</td>
<td>−55°C to +125°C</td>
</tr>
<tr>
<td>Capacitance Change with Reference to +25°C and 0 Vdc Applied (TCC)</td>
<td>±15%</td>
</tr>
<tr>
<td>Aging Rate (Maximum % Capacitance Loss/Decade Hour)</td>
<td>3.0%</td>
</tr>
</tbody>
</table>
| Dielectric Withstanding Voltage (DWV)                                | 150% of rated voltage for voltage rating of < 1000V  
120% of rated voltage for voltage rating of ≥ 1000V  
(5±1 seconds and charge/discharge not exceeding 50mA) |
| Dissipation Factor (DF) Maximum Limit at 25°C                        | 2.5%                       |
| Insulation Resistance (IR) Minimum Limit at 25°C                    | 100 megohm microfarads or 10GΩ  
(500VDC applied for 120±5 seconds at 25°C) |

1 Regarding Aging Rate: Capacitance measurements (including tolerance) are indexed to a referee time of 1,000 hours.

2 DWV is the voltage a capacitor can withstand (survive) for a short period of time. It exceeds the nominal and continuous working voltage of the capacitor.

3 Capacitance and dissipation factor (DF) measured under the following conditions:
   1kHz ± 50Hz and 1.0 ± 0.2 Vrms if capacitance ≤10µF  
   120Hz ± 10Hz and 0.5 ± 0.1 Vrms if capacitance >10µF

4 To obtain IR limit, divide MΩ-µF value by the capacitance and compare to GΩ limit. Select the lower of the two limits.

Note: When measuring capacitance it is important to ensure the set voltage level is held constant. The HP4284 & Agilent E4980 have a feature known as Automatic Level Control (ALC). The ALC feature should be switched to "ON".

Post Environmental Limits

<table>
<thead>
<tr>
<th>High Temperature Life, Biased Humidity, Moisture Resistance</th>
<th>Dielectric</th>
<th>Rated DC Voltage</th>
<th>Capacitance Value</th>
<th>Dissipation Factor (Maximum %)</th>
<th>Capacitance Shift</th>
<th>Insulation Resistance</th>
</tr>
</thead>
<tbody>
<tr>
<td>X7R</td>
<td>&gt; 25</td>
<td>All</td>
<td>3.0</td>
<td>±20%</td>
<td>10% of Initial Limit</td>
<td></td>
</tr>
<tr>
<td>16/25</td>
<td>5.0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>&lt; 16</td>
<td>7.5</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
Table 1 – Capacitance Range/Selection Waterfall (0603 – 1812 Case Sizes)

<table>
<thead>
<tr>
<th>Voltage Code</th>
<th>C0603W/V</th>
<th>C0805W/V</th>
<th>C1206W/V</th>
<th>C1210W/V</th>
<th>C1808W/V</th>
<th>C1812W/V</th>
<th>C1825W/V</th>
<th>C2220W/V</th>
<th>C2225W/V</th>
</tr>
</thead>
<tbody>
<tr>
<td>Rated Voltage (VDC)</td>
<td>500</td>
<td>630</td>
<td>1000</td>
<td>500</td>
<td>630</td>
<td>1000</td>
<td>500</td>
<td>630</td>
<td>1000</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Capacitance Tolerance</th>
<th>C</th>
<th>B</th>
<th>D</th>
<th>C</th>
<th>B</th>
<th>D</th>
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<th>B</th>
<th>D</th>
<th>C</th>
<th>B</th>
<th>D</th>
<th>C</th>
<th>B</th>
<th>C</th>
<th>B</th>
</tr>
</thead>
<tbody>
<tr>
<td>1,000 pF</td>
<td>J</td>
<td>K</td>
<td>M</td>
<td>CG</td>
<td>CG</td>
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<tr>
<td>1,200 pF</td>
<td>J</td>
<td>K</td>
<td>M</td>
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</tr>
<tr>
<td>1,500 pF</td>
<td>J</td>
<td>K</td>
<td>M</td>
<td>CG</td>
<td>CG</td>
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<td>1,800 pF</td>
<td>J</td>
<td>K</td>
<td>M</td>
<td>DG</td>
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<tr>
<td>2,200 pF</td>
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<td>M</td>
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<td>DG</td>
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<tr>
<td>2,700 pF</td>
<td>J</td>
<td>K</td>
<td>M</td>
<td>DG</td>
<td>DG</td>
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</tr>
<tr>
<td>3,300 pF</td>
<td>J</td>
<td>K</td>
<td>M</td>
<td>DG</td>
<td>DG</td>
<td>DG</td>
<td></td>
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</tr>
</tbody>
</table>

| Voltage Code | 0.10 µF | 104 | J | K | M | | | | | | | | | | |
| Voltage Code | 0.12 µF | 124 | J | K | M | | | | | | | | | | |
| Voltage Code | 0.15 µF | 154 | J | K | M | | | | | | | | | | |
| Voltage Code | 0.18 µF | 184 | J | K | M | | | | | | | | | | |
| Voltage Code | 0.22 µF | 224 | J | K | M | | | | | | | | | | |
| Voltage Code | 0.27 µF | 274 | J | K | M | | | | | | | | | | |
| Voltage Code | 0.33 µF | 334 | J | K | M | | | | | | | | | | |
| Voltage Code | 0.39 µF | 394 | J | K | M | | | | | | | | | | |
| Voltage Code | 0.47 µF | 474 | J | K | M | | | | | | | | | | |
| Voltage Code | 0.56 µF | 564 | J | K | M | | | | | | | | | | |

| Voltage Code | 0.10 µF | 104 | J | K | M | | | | | | | | | | |
| Voltage Code | 0.12 µF | 124 | J | K | M | | | | | | | | | | |
| Voltage Code | 0.15 µF | 154 | J | K | M | | | | | | | | | | |
| Voltage Code | 0.18 µF | 184 | J | K | M | | | | | | | | | | |
| Voltage Code | 0.22 µF | 224 | J | K | M | | | | | | | | | | |
| Voltage Code | 0.27 µF | 274 | J | K | M | | | | | | | | | | |
| Voltage Code | 0.33 µF | 334 | J | K | M | | | | | | | | | | |
| Voltage Code | 0.39 µF | 394 | J | K | M | | | | | | | | | | |
| Voltage Code | 0.47 µF | 474 | J | K | M | | | | | | | | | | |
| Voltage Code | 0.56 µF | 564 | J | K | M | | | | | | | | | | |

These products are protected under US Patents 8,885,319 B2 and 9,490,072 B2, other patents pending, and any foreign counterparts. KEMET reserves the right to substitute product with an improved temperature characteristic, tighter capacitance tolerance and/or higher voltage capability within the same form factor (configuration and dimensions).
### Table 2A – Chip Thickness/Tape & Reel Packaging Quantities

<table>
<thead>
<tr>
<th>Thickness Code</th>
<th>Case Size</th>
<th>Thickness ± Range (mm)</th>
<th>Paper Quantity¹</th>
<th>Plastic Quantity</th>
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<tbody>
<tr>
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<td>7” Reel</td>
<td>13” Reel</td>
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<tr>
<td>CG</td>
<td>603</td>
<td>0.80 ± 0.10*</td>
<td>4000</td>
<td>15000</td>
</tr>
<tr>
<td>DG</td>
<td>805</td>
<td>1.25 ± 0.15</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>EJ</td>
<td>1206</td>
<td>1.70 ± 0.20</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>FZ</td>
<td>1210</td>
<td>1.25 ± 0.20</td>
<td>0</td>
<td>0</td>
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<td>FU</td>
<td>1210</td>
<td>1.55 ± 0.20</td>
<td>0</td>
<td>0</td>
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<tr>
<td>FK</td>
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<td>2.10 ± 0.20</td>
<td>0</td>
<td>0</td>
</tr>
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<td>FS</td>
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<td>2.50 ± 0.30</td>
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<td>0</td>
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<tr>
<td>LE</td>
<td>1808</td>
<td>1.00 ± 0.10</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>LA</td>
<td>1808</td>
<td>1.40 ± 0.15</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>LB</td>
<td>1808</td>
<td>1.60 ± 0.15</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>LC</td>
<td>1808</td>
<td>2.00 ± 0.15</td>
<td>0</td>
<td>0</td>
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<td>0</td>
<td>0</td>
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<td>GE</td>
<td>1812</td>
<td>1.30 ± 0.10</td>
<td>0</td>
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<tr>
<td>GF</td>
<td>1812</td>
<td>1.50 ± 0.10</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>GJ</td>
<td>1812</td>
<td>1.70 ± 0.15</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>GL</td>
<td>1812</td>
<td>1.90 ± 0.20</td>
<td>0</td>
<td>0</td>
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<tr>
<td>GS</td>
<td>1812</td>
<td>2.10 ± 0.20</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>GC</td>
<td>1812</td>
<td>1.10 ± 0.10</td>
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<td>0</td>
<td>0</td>
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<tr>
<td>GK</td>
<td>1812</td>
<td>1.60 ± 0.20</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>GN</td>
<td>1812</td>
<td>1.70 ± 0.20</td>
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<td>0</td>
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<tr>
<td>HE</td>
<td>1825</td>
<td>1.40 ± 0.15</td>
<td>0</td>
<td>0</td>
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<tr>
<td>HG</td>
<td>1825</td>
<td>1.60 ± 0.20</td>
<td>0</td>
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</tr>
<tr>
<td>HJ</td>
<td>1825</td>
<td>2.00 ± 0.20</td>
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<tr>
<td>HK</td>
<td>1825</td>
<td>2.50 ± 0.20</td>
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<td>0</td>
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<tr>
<td>JE</td>
<td>2220</td>
<td>1.40 ± 0.15</td>
<td>0</td>
<td>0</td>
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<tr>
<td>JK</td>
<td>2220</td>
<td>1.60 ± 0.20</td>
<td>0</td>
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<td>JL</td>
<td>2220</td>
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<td>JN</td>
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<tr>
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<td>KF</td>
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<td>KH</td>
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<td>2.00 ± 0.20</td>
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<td>KJ</td>
<td>2225</td>
<td>2.50 ± 0.20</td>
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</table>

Package quantity based on finished chip thickness specifications.

¹If ordering using the 2 mm Tape and Reel pitch option, the packaging quantity outlined in the table above will be doubled. This option is limited to EIA 0603 (1608 metric) case size devices. For more information regarding 2 mm pitch option see "Tape & Reel Packaging Information."
Table 2B – Bulk Packaging Quantities

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<tr>
<th>Packaging Type</th>
<th>Loose Packaging</th>
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<tbody>
<tr>
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<td>Bulk Bag (default)</td>
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<tr>
<td>Packaging C-Spec¹</td>
<td>N/A²</td>
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<table>
<thead>
<tr>
<th>Case Size</th>
<th>Packaging Quantities (pieces/unit packaging)</th>
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<td>EIA (in)</td>
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<td>0.0625</td>
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<tr>
<td>0603</td>
<td>0.125</td>
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<tr>
<td>0805</td>
<td>0.1875</td>
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<td>1206</td>
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<td>1210</td>
<td>0.307</td>
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<td>1808</td>
<td>0.7125</td>
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<td>1812</td>
<td>0.7125</td>
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<td>0.750</td>
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<td>2220</td>
<td>0.89375</td>
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<td>2225</td>
<td>0.89375</td>
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</table>

¹ The “Packaging C-Spec” is a 4 to 8 digit code which identifies the packaging type and/or product grade. When ordering, the proper code must be included in the 15th through 22nd character positions of the ordering code. See “Ordering Information” section of this document for further details. Commercial Grade product ordered without a packaging C-Spec will default to our standard “Bulk Bag” packaging. Contact KEMET if you require a bulk bag packaging option for Automotive Grade products.

² A packaging C-Spec (see note 1 above) is not required for “Bulk Bag” packaging (excluding Anti-Static Bulk Bag and Automotive Grade products). The 15th through 22nd character positions of the ordering code should be left blank. All product ordered without a packaging C-Spec will default to our standard “Bulk Bag” packaging.
Table 3A – Land Pattern Design Recommendations per IPC–7351 – Standard Termination

<table>
<thead>
<tr>
<th>EIA Size Code</th>
<th>Metric Size Code</th>
<th>Density Level A: Maximum (Most)</th>
<th>Density Level B: Median (Nominal)</th>
<th>Density Level C: Minimum (Least)</th>
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<td>C</td>
<td>Y</td>
<td>X</td>
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<td>1.15</td>
<td>1.10</td>
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<td>1.00</td>
<td>1.35</td>
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<td>5.50</td>
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<td>5664</td>
<td>2.70</td>
<td>1.70</td>
<td>6.90</td>
</tr>
</tbody>
</table>

**Density Level A:** For low-density product applications. Recommended for wave solder applications and provides a wider process window for reflow solder processes. KEMET only recommends wave soldering of EIA 0603, 0805 and 1206 case sizes.

**Density Level B:** For products with a moderate level of component density. Provides a robust solder attachment condition for reflow solder processes.

**Density Level C:** For high component density product applications. Before adapting the minimum land pattern variations the user should perform qualification testing based on the conditions outlined in IPC Standard 7351 (IPC–7351).

Image below based on Density Level B for an EIA 1210 case size.
### Table 3B – Land Pattern Design Recommendations per IPC–7351 – Flexible Termination

<table>
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<tr>
<th>EIA Size Code</th>
<th>Metric Size Code</th>
<th>Density Level A: Maximum (Most) Land Protrusion (mm)</th>
<th>Density Level B: Median (Nominal) Land Protrusion (mm)</th>
<th>Density Level C: Minimum (Least) Land Protrusion (mm)</th>
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<tbody>
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<td>C Y X V1 V2</td>
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<td>0.85 1.25 1.10 4.00 2.10</td>
<td>0.75 1.05 1.00 3.10 1.50</td>
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<td>2012</td>
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<td>3216</td>
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<td>1.49 1.42 2.91 4.95 3.41</td>
<td>1.39 1.22 2.81 4.25 3.11</td>
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<td>4520</td>
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<td>2.20 1.55 2.20 6.50 2.70</td>
<td>2.10 1.35 2.10 5.80 2.40</td>
</tr>
<tr>
<td>1812</td>
<td>4532</td>
<td>2.10 1.80 3.60 7.00 4.60</td>
<td>2.00 1.60 3.50 6.10 4.00</td>
<td>1.90 1.40 3.40 5.40 3.70</td>
</tr>
<tr>
<td>1825</td>
<td>4564</td>
<td>2.15 1.80 6.90 7.10 7.90</td>
<td>2.05 1.60 6.80 6.20 7.30</td>
<td>1.95 1.40 6.70 5.50 7.00</td>
</tr>
<tr>
<td>2220</td>
<td>5650</td>
<td>2.85 2.10 5.50 8.80 6.50</td>
<td>2.75 1.90 5.40 7.90 5.90</td>
<td>2.65 1.70 5.30 7.20 5.60</td>
</tr>
<tr>
<td>2225</td>
<td>5664</td>
<td>2.85 2.10 6.90 8.80 7.90</td>
<td>2.75 1.90 6.80 7.90 7.30</td>
<td>2.65 1.70 6.70 7.20 7.00</td>
</tr>
</tbody>
</table>

**Density Level A:** For low-density product applications. Recommended for wave solder applications and provides a wider process window for reflow solder processes. KEMET only recommends wave soldering of EIA 0603, 0805 and 1206 case sizes.

**Density Level B:** For products with a moderate level of component density. Provides a robust solder attachment condition for reflow solder processes.

**Density Level C:** For high component density product applications. Before adapting the minimum land pattern variations the user should perform qualification testing based on the conditions outlined in IPC Standard 7351 (IPC–7351).

---

*Image below based on Density Level B for an EIA 1210 case size.*
Soldering Process

**Recommended Soldering Technique:**
- Solder wave or solder reflow for EIA case sizes 0603, 0805 and 1206
- All other EIA case sizes are limited to solder reflow only

**Recommended Reflow Soldering Profile:**
The KEMET families of surface mount multilayer ceramic capacitors (SMD MLCCs) are compatible with wave (single or dual), convection, IR or vapor phase reflow techniques. Preheating of these components is recommended to avoid extreme thermal stress. KEMET's recommended profile conditions for convection and IR reflow reflect the profile conditions of the IPC/J-STD-020 standard for moisture sensitivity testing. These devices can safely withstand a maximum of three reflow passes at these conditions.

<table>
<thead>
<tr>
<th>Profile Feature</th>
<th>Termination Finish</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>SnPb</td>
</tr>
<tr>
<td><strong>Preheat/Soak</strong></td>
<td></td>
</tr>
<tr>
<td>Temperature Minimum (T\text{Smin})</td>
<td>100°C</td>
</tr>
<tr>
<td>Temperature Maximum (T\text{Smax})</td>
<td>150°C</td>
</tr>
<tr>
<td>Time (tS) from T\text{Smin} to T\text{Smax}</td>
<td>60 – 120 seconds</td>
</tr>
<tr>
<td><strong>Ramp-Up Rate (T_i to T_p)</strong></td>
<td></td>
</tr>
<tr>
<td></td>
<td>3°C/second maximum</td>
</tr>
<tr>
<td><strong>Liquidous Temperature (T_L)</strong></td>
<td>183°C</td>
</tr>
<tr>
<td>Time Above Liquidous (t_L)</td>
<td>60 – 150 seconds</td>
</tr>
<tr>
<td><strong>Peak Temperature (T_P)</strong></td>
<td>235°C</td>
</tr>
<tr>
<td>Time Within 5°C of Maximum Peak Temperature (t_P)</td>
<td>20 seconds maximum</td>
</tr>
<tr>
<td><strong>Ramp-Down Rate (T_p to T_L)</strong></td>
<td>6°C/second maximum</td>
</tr>
<tr>
<td>Time 25°C to Peak Temperature</td>
<td>6 minutes maximum</td>
</tr>
</tbody>
</table>

*Note: All temperatures refer to the center of the package, measured on the capacitor body surface that is facing up during assembly reflow.*
Table 4 – Performance & Reliability: Test Methods and Conditions

<table>
<thead>
<tr>
<th>Stress</th>
<th>Reference</th>
<th>Test or Inspection Method</th>
</tr>
</thead>
<tbody>
<tr>
<td>Terminal Strength</td>
<td>JIS–C–6429</td>
<td>Appendix 1, Note: Force of 1.8 kg for 60 seconds.</td>
</tr>
<tr>
<td>Board Flex</td>
<td>JIS–C–6429</td>
<td>Appendix 2, Note: Standard termination system – 2.0 mm (minimum) for all except 3 mm for C0G. Flexible termination system – 3.0 mm (minimum).</td>
</tr>
<tr>
<td>Solderability</td>
<td>J–STD–002</td>
<td>Magnification 50 X. Conditions:</td>
</tr>
<tr>
<td></td>
<td></td>
<td>a) Method B, 4 hours at 155°C, dry heat at 235°C</td>
</tr>
<tr>
<td></td>
<td></td>
<td>b) Method B, category 3, at 215°C</td>
</tr>
<tr>
<td></td>
<td></td>
<td>c) Method D, category 3, at 260°C</td>
</tr>
<tr>
<td>Temperature Cycling</td>
<td>JESD22 Method JA–104</td>
<td>1,000 cycles (−55°C to +125°C). Measurement at 24 hours ±4 hours after test conclusion.</td>
</tr>
<tr>
<td>Biased Humidity</td>
<td>MIL–STD–202 Method 103</td>
<td>Load humidity: 1,000 hours 85°C/85% RH and 200 VDC maximum. Add 100 K ohm resistor. Measurement at 24 hours ±4 hours after test conclusion.</td>
</tr>
<tr>
<td></td>
<td>MIL–STD–202 Method 104</td>
<td>Low volt humidity: 1,000 hours 85°C/85% RH and 1.5 V. Add 100 K ohm resistor. Measurement at 24 hours ±4 hours after test conclusion.</td>
</tr>
<tr>
<td>High Temperature Life</td>
<td>MIL–STD–202 Method 108</td>
<td>1,000 hours at 125°C (85°C for X5R, Z5U and Y5V) with 1.2 X rated voltage applied.</td>
</tr>
<tr>
<td>Storage Life</td>
<td>MIL–STD–202 Method 109</td>
<td>150°C, 0 VDC for 1,000 hours.</td>
</tr>
<tr>
<td>Vibration</td>
<td>MIL–STD–202 Method 204</td>
<td>5 g’s for 20 minutes, 12 cycles each of 3 orientations. Note: Use 8” X 5” PCB 0.031” thick 7 secure points on one long side and 2 secure points at corners of opposite sides. Parts mounted within 2” from any secure point. Test from 10 – 2,000 Hz</td>
</tr>
<tr>
<td>Mechanical Shock</td>
<td>MIL–STD–202 Method 213</td>
<td>Figure 1 of Method 213, Condition F.</td>
</tr>
<tr>
<td>Resistance to Solvents</td>
<td>MIL–STD–202 Method 215</td>
<td>Add aqueous wash chemical, OKEM Clean or equivalent.</td>
</tr>
</tbody>
</table>

Storage & Handling

Ceramic chip capacitors should be stored in normal working environments. While the chips themselves are quite robust in other environments, solderability will be degraded by exposure to high temperatures, high humidity, corrosive atmospheres, and long term storage. In addition, packaging materials will be degraded by high temperature – reels may soften or warp and tape peel force may increase. KEMET recommends that maximum storage temperature not exceed 40°C and maximum storage humidity not exceed 70% relative humidity. Temperature fluctuations should be minimized to avoid condensation on the parts and atmospheres should be free of chlorine and sulfur bearing compounds. For optimized solderability chip stock should be used promptly, preferably within 1.5 years of receipt.
Surface Mount Multilayer Ceramic Chip Capacitors (SMD MLCCs)
ArcShield Technology, High Voltage, X7R Dielectric, 500 – 1,000 VDC (Commercial & Automotive Grade)

Construction – Standard Termination

Detailed Cross Section

Construction – Flexible Termination

Detailed Cross Section
Capacitor Marking (Optional):

These surface mount multilayer ceramic capacitors are normally supplied unmarked. If required, they can be marked as an extra cost option. Marking is available on most KEMET devices, but must be requested using the correct ordering code identifier(s). If this option is requested, two sides of the ceramic body will be laser marked with a “K” to identify KEMET, followed by two characters (per EIA–198 - see table below) to identify the capacitance value. EIA 0603 case size devices are limited to the “K” character only.

Laser marking option is not available on:
- C0G, ultra stable X8R and Y5V dielectric devices.
- EIA 0402 case size devices.
- EIA 0603 case size devices with flexible termination option.
- KPS commercial and automotive grade stacked devices.
- X7R dielectric products in capacitance values outlined below.

Marking appears in legible contrast. Illustrated below is an example of an MLCC with laser marking of “KA8”, which designates a KEMET device with rated capacitance of 100 µF. Orientation of marking is vendor optional.

<table>
<thead>
<tr>
<th>EIA Case Size</th>
<th>Metric Size Code</th>
<th>Capacitance</th>
</tr>
</thead>
<tbody>
<tr>
<td>0603</td>
<td>1608</td>
<td>≤ 170 pF</td>
</tr>
<tr>
<td>0805</td>
<td>2012</td>
<td>≤ 150 pF</td>
</tr>
<tr>
<td>1206</td>
<td>3216</td>
<td>≤ 910 pF</td>
</tr>
<tr>
<td>1210</td>
<td>3225</td>
<td>≤ 2,000 pF</td>
</tr>
<tr>
<td>1808</td>
<td>4520</td>
<td>≤ 3,900 pF</td>
</tr>
<tr>
<td>1812</td>
<td>4532</td>
<td>≤ 6,700 pF</td>
</tr>
<tr>
<td>1825</td>
<td>4564</td>
<td>≤ 0.018 µF</td>
</tr>
<tr>
<td>2220</td>
<td>5650</td>
<td>≤ 0.027 µF</td>
</tr>
<tr>
<td>2225</td>
<td>5664</td>
<td>≤ 0.033 µF</td>
</tr>
</tbody>
</table>
## Capacitor Marking (Optional) cont.

<table>
<thead>
<tr>
<th>Character</th>
<th>Capacitance (pF) For Various Alpha/Numeral Identifiers</th>
</tr>
</thead>
<tbody>
<tr>
<td>Alpha</td>
<td>Numerals</td>
</tr>
<tr>
<td>A</td>
<td>0.10</td>
</tr>
<tr>
<td>B</td>
<td>0.11</td>
</tr>
<tr>
<td>C</td>
<td>0.12</td>
</tr>
<tr>
<td>D</td>
<td>0.13</td>
</tr>
<tr>
<td>E</td>
<td>0.15</td>
</tr>
<tr>
<td>F</td>
<td>0.16</td>
</tr>
<tr>
<td>G</td>
<td>0.18</td>
</tr>
<tr>
<td>H</td>
<td>0.20</td>
</tr>
<tr>
<td>J</td>
<td>0.22</td>
</tr>
<tr>
<td>K</td>
<td>0.24</td>
</tr>
<tr>
<td>L</td>
<td>0.27</td>
</tr>
<tr>
<td>M</td>
<td>0.30</td>
</tr>
<tr>
<td>N</td>
<td>0.33</td>
</tr>
<tr>
<td>P</td>
<td>0.36</td>
</tr>
<tr>
<td>Q</td>
<td>0.39</td>
</tr>
<tr>
<td>R</td>
<td>0.43</td>
</tr>
<tr>
<td>S</td>
<td>0.47</td>
</tr>
<tr>
<td>T</td>
<td>0.51</td>
</tr>
<tr>
<td>U</td>
<td>0.56</td>
</tr>
<tr>
<td>V</td>
<td>0.62</td>
</tr>
<tr>
<td>W</td>
<td>0.68</td>
</tr>
<tr>
<td>X</td>
<td>0.75</td>
</tr>
<tr>
<td>Y</td>
<td>0.82</td>
</tr>
<tr>
<td>Z</td>
<td>0.91</td>
</tr>
<tr>
<td>a</td>
<td>0.25</td>
</tr>
<tr>
<td>b</td>
<td>0.35</td>
</tr>
<tr>
<td>d</td>
<td>0.40</td>
</tr>
<tr>
<td>e</td>
<td>0.45</td>
</tr>
<tr>
<td>f</td>
<td>0.50</td>
</tr>
<tr>
<td>m</td>
<td>0.60</td>
</tr>
<tr>
<td>n</td>
<td>0.70</td>
</tr>
<tr>
<td>t</td>
<td>0.80</td>
</tr>
<tr>
<td>y</td>
<td>0.90</td>
</tr>
</tbody>
</table>
Tape & Reel Packaging Information

KEMET offers multilayer ceramic chip capacitors packaged in 8, 12 and 16 mm tape on 7" and 13" reels in accordance with EIA Standard 481. This packaging system is compatible with all tape-fed automatic pick and place systems. See Table 2 for details on reeling quantities for commercial chips.

Table 5 – Carrier Tape Configuration, Embossed Plastic & Punched Paper (mm)

<table>
<thead>
<tr>
<th>EIA Case Size</th>
<th>Tape Size (W)*</th>
<th>Embossed Plastic</th>
<th>Punched Paper</th>
<th>Packaging Type/Options</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>7&quot; Reel</td>
<td>13&quot; Reel</td>
<td>7&quot; Reel</td>
<td>13&quot; Reel</td>
</tr>
<tr>
<td></td>
<td>Pitch (P₁)*</td>
<td>Pitch (P₁)*</td>
<td></td>
<td></td>
</tr>
<tr>
<td>01005 – 0402</td>
<td>8</td>
<td>2</td>
<td>2</td>
<td></td>
</tr>
<tr>
<td>0603</td>
<td>8</td>
<td>2/4</td>
<td>2/4</td>
<td></td>
</tr>
<tr>
<td>0805</td>
<td>8</td>
<td>4</td>
<td>4</td>
<td></td>
</tr>
<tr>
<td>1206 – 1210</td>
<td>8</td>
<td>4</td>
<td>4</td>
<td></td>
</tr>
<tr>
<td>1805 – 1808</td>
<td>12</td>
<td>4</td>
<td>4</td>
<td></td>
</tr>
<tr>
<td>≥ 1812</td>
<td>12</td>
<td>8</td>
<td>8</td>
<td></td>
</tr>
<tr>
<td>KPS 1210</td>
<td>12</td>
<td>8</td>
<td>8</td>
<td></td>
</tr>
<tr>
<td>KPS 1812 and 2220</td>
<td>16</td>
<td>12</td>
<td>12</td>
<td></td>
</tr>
<tr>
<td>Array 0612</td>
<td>8</td>
<td>4</td>
<td>4</td>
<td></td>
</tr>
</tbody>
</table>

*Refer to Figures 1 and 2 for W and P₁ carrier tape reference locations.
*Refer to Tables 6 and 7 for tolerance specifications.

New 2 mm Pitch Reel Options*

<table>
<thead>
<tr>
<th>Packaging Ordering Code (C-Spec)</th>
<th>Packaging Type/Options</th>
</tr>
</thead>
<tbody>
<tr>
<td>C-3190</td>
<td>Automotive grade 7&quot; reel unmarked</td>
</tr>
<tr>
<td>C-3191</td>
<td>Automotive grade 13&quot; reel unmarked</td>
</tr>
<tr>
<td>C-7081</td>
<td>Commercial grade 7&quot; reel unmarked</td>
</tr>
<tr>
<td>C-7082</td>
<td>Commercial grade 13&quot; reel unmarked</td>
</tr>
</tbody>
</table>

* 2 mm pitch reel only available for 0603 EIA case size.
2 mm pitch reel for 0805 EIA case size under development.

Benefits of Changing from 4 mm to 2 mm Pitching Spacing

- Lower placement costs.
- Double the parts on each reel results in fewer reel changes and increased efficiency.
- Fewer reels result in lower packaging, shipping and storage costs, reducing waste.
Figure 1 – Embossed (Plastic) Carrier Tape Dimensions

![Figure 1 – Embossed (Plastic) Carrier Tape Dimensions](image)

Table 6 – Embossed (Plastic) Carrier Tape Dimensions
Metric will govern

<table>
<thead>
<tr>
<th>Constant Dimensions – Millimeters (Inches)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Tape Size</td>
</tr>
<tr>
<td>-----------</td>
</tr>
<tr>
<td>8 mm</td>
</tr>
<tr>
<td>12 mm</td>
</tr>
<tr>
<td>16 mm</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Variable Dimensions – Millimeters (Inches)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Tape Size</td>
</tr>
<tr>
<td>-----------</td>
</tr>
<tr>
<td>8 mm</td>
</tr>
<tr>
<td>12 mm</td>
</tr>
<tr>
<td>16 mm</td>
</tr>
</tbody>
</table>

1. The embossment hole location shall be measured from the sprocket hole controlling the location of the embossment. Dimensions of the embossment location and the hole location shall be applied independently of each other.
2. The tape with or without components shall pass around R without damage (see Figure 6.)
3. If $S_1 < 1.0$ mm, there may not be enough area for a cover tape to be properly applied (see EIA Standard 481, paragraph 4.3, section b.)
4. $B_1$ dimension is a reference dimension for tape feeder clearance only.
5. The cavity defined by $A_0B_0$ and $K_0$ shall surround the component with sufficient clearance that:
   (a) the component does not protrude above the top surface of the carrier tape.
   (b) the component can be removed from the cavity in a vertical direction without mechanical restriction, after the top cover tape has been removed.
   (c) rotation of the component is limited to $20^\circ$ maximum for 8 and 12 mm tapes and $10^\circ$ maximum for 16 mm tapes (see Figure 3.)
   (d) lateral movement of the component is restricted to 0.5 mm maximum for 8 and 12 mm wide tape and to 1.0 mm maximum for 16 mm tape (see Figure 4.)
   (e) for KPS product, $A_0$ and $B_0$ are measured on a plane 0.3 mm above the bottom of the pocket.
   (f) see addendum in EIA Standard 481 for standards relating to more precise taping requirements.
Figure 2 – Punched (Paper) Carrier Tape Dimensions

Table 7 – Punched (Paper) Carrier Tape Dimensions
Metric will govern

<table>
<thead>
<tr>
<th>Tape Size</th>
<th>D₀</th>
<th>E₁</th>
<th>P₀</th>
<th>P₂</th>
<th>Tₙ Maximum</th>
<th>G Minimum</th>
<th>R Reference Note 2</th>
</tr>
</thead>
<tbody>
<tr>
<td>8 mm</td>
<td>1.5 ±0.10 -0.0 (0.059 ±0.004 -0.0)</td>
<td>1.75 ±0.10 (0.069 ±0.004)</td>
<td>4.0 ±0.10 (0.157 ±0.004)</td>
<td>2.0 ±0.05 (0.079 ±0.002)</td>
<td>0.10 (0.004) maximum</td>
<td>0.75 (0.030)</td>
<td>25 (0.984)</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Tape Size</th>
<th>Pitch</th>
<th>E2 Minimum</th>
<th>F</th>
<th>P₁</th>
<th>T Maximum</th>
<th>W Maximum</th>
<th>A₀ B₀</th>
</tr>
</thead>
<tbody>
<tr>
<td>8 mm</td>
<td>Half (2 mm)</td>
<td>6.25 (0.246)</td>
<td>3.5 ±0.05 (0.138 ±0.002)</td>
<td>2.0 ±0.05 (0.079 ±0.002)</td>
<td>1.1 (0.098)</td>
<td>8.3 (0.327)</td>
<td>Note 1</td>
</tr>
<tr>
<td>8 mm</td>
<td>Single (4 mm)</td>
<td></td>
<td></td>
<td>4.0 ±0.10 (0.157 ±0.004)</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

1. The cavity defined by A₀, B₀ and T shall surround the component with sufficient clearance that:
   a) the component does not protrude beyond either surface of the carrier tape.
   b) the component can be removed from the cavity in a vertical direction without mechanical restriction, after the top cover tape has been removed.
   c) rotation of the component is limited to 20° maximum (see Figure 3.)
   d) lateral movement of the component is restricted to 0.5 mm maximum (see Figure 4.)
   e) see addendum in EIA Standard 481 for standards relating to more precise taping requirements.

2. The tape with or without components shall pass around R without damage (see Figure 6.)
Packaging Information Performance Notes

1. **Cover Tape Break Force**: 1.0 kg minimum.
2. **Cover Tape Peel Strength**: The total peel strength of the cover tape from the carrier tape shall be:

<table>
<thead>
<tr>
<th>Tape Width</th>
<th>Peel Strength</th>
</tr>
</thead>
<tbody>
<tr>
<td>8 mm</td>
<td>0.1 to 1.0 newton (10 to 100 gf)</td>
</tr>
<tr>
<td>12 and 16 mm</td>
<td>0.1 to 1.3 newton (10 to 130 gf)</td>
</tr>
</tbody>
</table>

The direction of the pull shall be opposite the direction of the carrier tape travel. The pull angle of the carrier tape shall be 165° to 180° from the plane of the carrier tape. During peeling, the carrier and/or cover tape shall be pulled at a velocity of 300 ±10 mm/minute.

3. **Labeling**: Bar code labeling (standard or custom) shall be on the side of the reel opposite the sprocket holes. Refer to EIA Standards 556 and 624.

**Figure 3 – Maximum Component Rotation**

**Figure 4 – Maximum Lateral Movement**

**Figure 5 – Bending Radius**
Figure 6 – Reel Dimensions

Table 8 – Reel Dimensions

Metric will govern

<table>
<thead>
<tr>
<th>Tape Size</th>
<th>Constant Dimensions – Millimeters (Inches)</th>
<th>Variable Dimensions – Millimeters (Inches)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Tape Size</td>
<td>A (See Note)</td>
</tr>
<tr>
<td>8 mm</td>
<td>178 ±0.20</td>
<td>(7.008 ±0.008)</td>
</tr>
<tr>
<td>12 mm</td>
<td>330 ±0.20</td>
<td>(13.000 ±0.008)</td>
</tr>
<tr>
<td>16 mm</td>
<td>18.4 +2.0/−0.0</td>
<td></td>
</tr>
</tbody>
</table>
Figure 7 – Tape Leader & Trailer Dimensions

- Embossed Carrier
- Punched Carrier: 8 mm & 12 mm only
- Carrier Tape
- Round Sprocket Holes
- START
- Top Cover Tape
- Trailer: 160 mm minimum
- Components
- Elongated Sprocket Holes
  - (32 mm tape and wider)
- Punch Carrier
  - 8 mm & 12 mm only
- Top Cover Tap

Figure 8 – Maximum Camber

- Elongated Sprocket Holes
  - (32 mm & wider tapes)
- Carrier Tape
- Round Sprocket Holes
- 1 mm maximum, either direction
- Straight Edge
- 250 mm
KEMET Electronics Corporation Sales Offices

For a complete list of our global sales offices, please visit www.kemet.com/sales.

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